

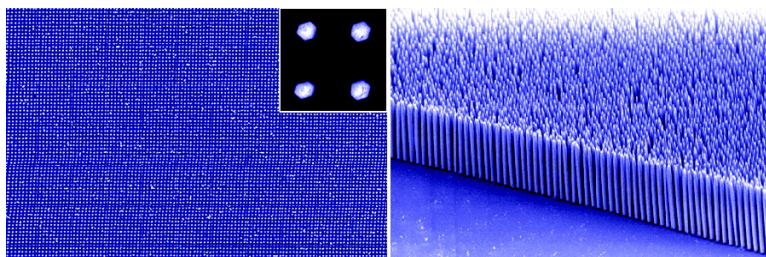
Communication

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Patterned Growth of Vertically Aligned ZnO Nanowire Arrays on Inorganic Substrates at Low Temperature without Catalyst

Sheng Xu,[†] Yaguang Wei,[†] Melanie Kirkham,[†] Jin Liu,[†] Wenjie Mai,[†] Dragomir Davidovic,[‡] Robert L. Snyder,[†] and Zhong Lin Wang^{*,†}

School of Material Science and Engineering, School of Physics, Georgia Institute of Technology, Atlanta, Georgia 30332

Received September 2, 2008; E-mail: zhong.wang@mse.gatech.edu

Vertically aligned ZnO nanowires (NWs) have a variety of astonishing applications in electronics as well as optoelectronic and electromechanical nanodevices, such as solar cells,¹ field emission devices,^{2,3} UV lasers,^{4,5} light emitting diodes,⁶ and piezo-nanogenerators.^{7,8} Growth of aligned ZnO NWs has been achieved on GaN via physical vapor deposition (PVD) at a high temperature of 500–800 °C, with the use of gold as a catalyst,⁹ in which the catalyst initiated and guided the growth, and the epitaxial orientation relationship between the ZnO NWs and the GaN substrate led to the aligned growth. This strategy may have the risk of introducing catalyst residual atoms into the ZnO NWs, which is incompatible with silicon technology. Patterned growth of aligned ZnO NWs has also been accomplished by PVD using nanosphere-masking lithography on sapphire,^{10,11} which was conducted at high growth temperature, and therefore was not compatible with many other materials. Metal–organic vapor-phase epitaxial growth¹² and template-assisted growth¹³ have also been employed for the fabrication of vertically aligned ZnO NWs. Yi et al. have demonstrated a technique of growing periodically arranged ZnO nanorod arrays on a silicon substrate coated with ZnO seeds by electron beam lithography (EBL).¹⁴ But their nanorods were thick in diameter and had a rather small aspect ratio,¹⁵ which greatly limited their applications.

To grow high-quality ZnO NW arrays, we need an approach that can meet the following three requirements. First, the growth has to be at low temperature so that the NWs can be integrated with general substrates. Second, the NWs have to be grown following a designed pattern, with a high degree of control in size, orientation, dimensionality, uniformity, and possibly shape. Finally, the catalyst may need to be eliminated for integration with silicon based technology. In this paper, we demonstrate an approach that meets all of these requirements. Our method combines EBL and a low temperature hydrothermal method to achieve patterned and aligned growth of ZnO NWs at <100 °C on general inorganic substrates, such as Si and GaN, without using a catalyst. This approach opens up the possibility of creating patterned, vertically aligned one-dimensional ZnO nanostructures for many applications such as sensor arrays, piezoelectric antenna arrays, two-dimensional photonic crystals, integrated circuit (IC) interconnects, and nanogenerators.

Detailed experimental methods are described in the Supporting Information (SI). The nonepitaxial growth of ZnO NW arrays on Si wafers (or any other inorganic substrates) was assisted by a thin film of polycrystalline ZnO seeds. EBL was employed to generate a patterned mask, which was made of poly(methyl methacrylate) (PMMA), over the ZnO seed film. The hydrothermal growth

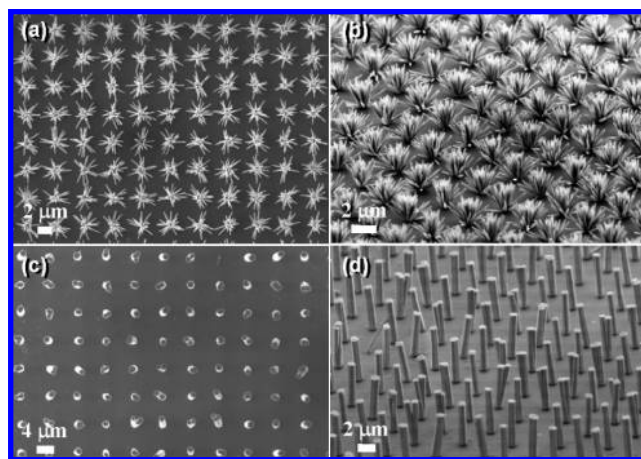


Figure 1. (a) Top view and (b) 60° tilt view of the ZnO NW arrays on a Si wafer grown at 70 °C. (c) Top view and (d) 60° tilt view of the ZnO NW arrays on a Si wafer grown at 95 °C.

mechanism has been well understood and documented.^{16–18} One thing worth noting is that during the hydrothermal growth process, by virtue of the solution surface tension, the substrate was put face-down floating on the nutrient solution surface (Figure S1 in the SI), to keep any precipitates from falling down onto the substrate from the solution body, which would otherwise inhibit the growth of the desired nanostructures or possibly initiate secondary growth. Even though the substrate was annealed at 300 °C for 20 min, the ZnO seed film on the substrate was still composed of many tiny crystals with random in-plane orientations, ranging from several nanometers to tens of nanometers in size (see SI Figure S2). The sizes of the EBL patterned openings in this study were all greater than 100 nm, in which a number of ZnO grains were exposed. Consequently, in most cases, multiple NWs would grow out of one single spot, as illustrated in Figure 1a,b. Under appropriate conditions, ZnO NWs in close proximity are inclined to have coalescence effects.^{19,20} In our experiments, by increasing the growth temperature from 70 °C to a relatively high growth temperature (95 °C), those multiple NWs from one single opening merged together, resulting in a thicker NW as shown in Figure 1c,d (see SI Figure S3 for half-merged NWs).

The epitaxial growth of aligned ZnO NW arrays on Si doped n-type GaN on a sapphire substrate²¹ was performed in an analogous way as on a Si wafer. The lattice mismatch between the wurzite GaN (0001) plane and wurzite ZnO (0001) plane is 1.8%,²² which led to a perfectly aligned growth of the ZnO NW array, as shown in Figure 2a,b. Because the as-grown NWs have a small diameter and a large aspect ratio, the sample needs a supercritical drying when the hydrothermal growth is complete.²³ Or else, when

[†] School of Material Science and Engineering.

[‡] School of Physics.

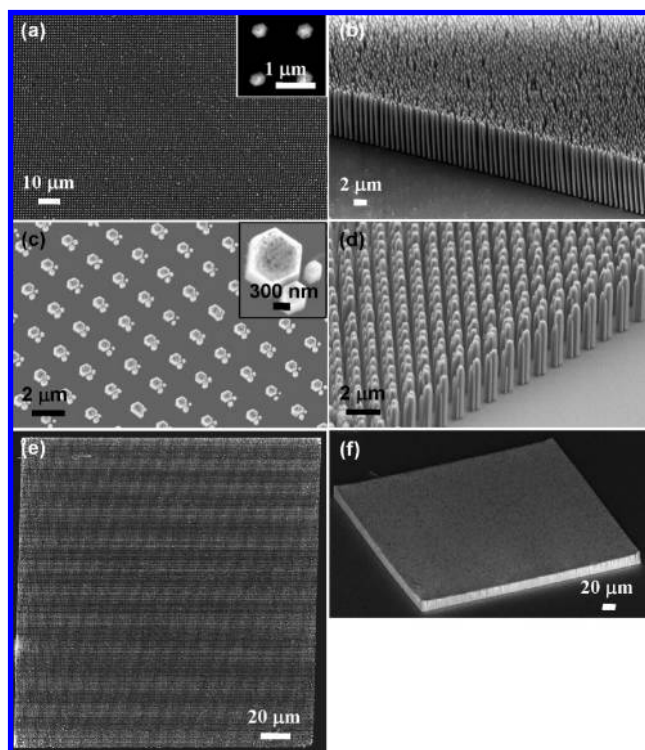


Figure 2. (a) Top view and (b) 60° tilt view of the ZnO NW arrays on a GaN substrate. (c) Top view and (d) 60° tilt view of the NWs growing out of 400, 200, and 100 nm sized holes. Insets are enlarged top view images. (e) Top view and (f) 60° tilt view of a 200 $\mu\text{m} \times 200 \mu\text{m}$ patterned NW arrays.

the liquid droplets on the NW arrays get evaporated and shrink in size, the surface tension of the droplet would sweep all the vertical NWs down onto the substrate.

An interesting phenomenon was noticed when we compared the EBL pattern before hydrothermal growth with that after hydrothermal growth (see SI Figure S4). Out of 100 nm sized holes the grown NWs were 300 nm in diameters. The width of the NWs was three times the diameter of the holes. We proposed a two-step growing process for the ZnO NWs as illustrated in Figure S5 in the SI. In the first step, the NW grew out of the photoresist hole with the same lateral dimension as confined by the hole. Then in the following step, there was no lateral confinement for it, and the NW could grow both vertically and laterally, but apparently faster in the vertical direction than in the lateral direction.

Even though the NWs would expand laterally when they grew out of the confining holes, we could still tune the width of the NWs in a very responsive and reliable fashion. As shown in Figure 2c,d, when we made an array of circles with 400, 200, and 100 nm in diameter, respectively, the developed NWs showed proportional sizes to the holes accordingly. In addition, the pitch between the roles and columns of the pattern could also be straight forwardly varied for different application purposes.

The crystal structure and vertical alignment of the as-grown ZnO NW arrays were examined by X-ray diffraction (XRD) pattern and rocking curve measurements. The θ - 2θ scanning results of the sample are shown in Figure 3, where the main peaks have been labeled. In the inset of Figure 3 is the θ -rocking curve for the peak at 34.393° of the ZnO (002) plane. It exhibits a full width at half-maximum value of 0.15°, which indicates almost perfect vertical alignment of the ZnO NWs on the GaN substrate.

In summary, we have developed a low temperature hydrothermal growth method to fabricate patterned and vertically aligned ZnO

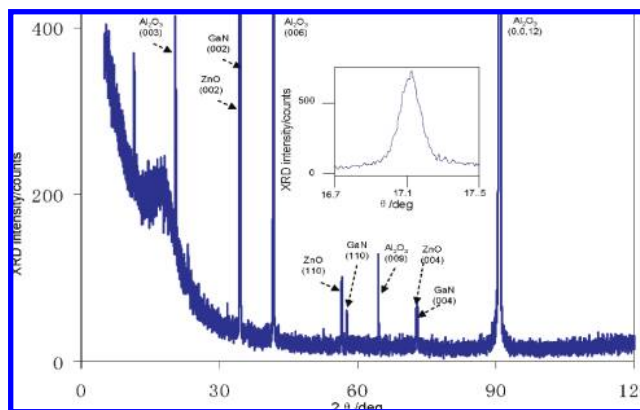


Figure 3. X-ray diffraction pattern of the as-grown ZnO nanowire arrays on GaN (001). Inset is the ZnO (002) peak rocking curve.

NW arrays on a general inorganic substrate that may or may not have an epitaxial orientation relationship with ZnO. On nonepitaxial substrates coated with ZnO seed film, we could grow ZnO NW arrays with one NW on one site by increasing the growth temperature. On epitaxial substrates, perfectly aligned highly uniform ZnO NW arrays could be grown with controllable NW size and tunable pattern pitch. It could also be easily scaled up, as shown in Figure 2e,f.

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Supporting Information Available: Experimental details about the synthesis and characterization of the nanowires arrays. This material is available free of charge via the Internet at <http://pubs.acs.org>.

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